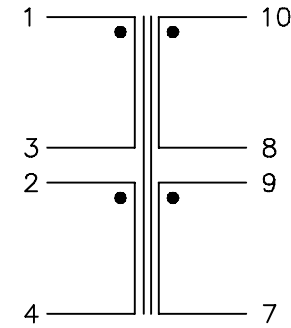
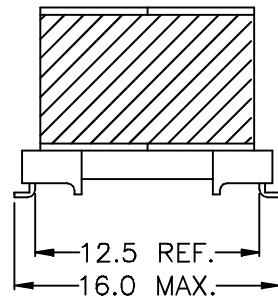
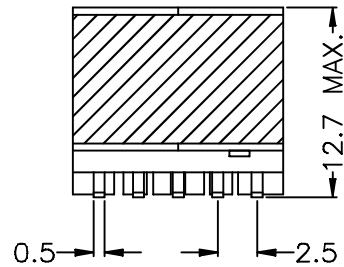


SUGGESTED P.C.B LAYOUT



* Rohs COMPLIANTE

ELECTRICAL CHARACTERISTICS :

1. DCR : PIN 1-4 = 2Ω MAX.(tie 2-3)
PIN 10-7 = 1.65Ω MAX.(tie 9-8)
2. INDUCTANCE (@10kHz, 0.1Vrms) :
PIN 1-4 (2-3 SHORT) = 400uH ±10%.
3. LEAKAGE INDUCTANCE (@100kHz, 0.1Vrms) :
PIN 1-4 (2-3, 10-9, 8-7 SHORT) = 1uH MAX.
4. INTERWINDING CAP. (@100kHz, 0.1Vrms) :
PIN 1-10 (2-3, 8-9 SHORT) = 110pF MAX.
5. THD (@25kHz, 2Vac) :
PIN 1-4 (2-3, 8-9 SHORT, 10-7 W/H 25 OHM) = -90dB typ.
6. TURN RATIO (@100kHz, 0.1Vrms) :
PIN 1-4 : 10-7 (2-3, 8-9 SHORT) = 2 ±2%.
7. HI-POT (@1875VAC, 0.5mA, 1SEC) :
PIN 1-10(2-3, 8-9 SHORT)

UNIT : mm

							TITLE VDSL2 TRANSFORMER	
							DWG. NO. ATS-1509R	
RELEASE					UNITS:		SAFETY	
NO: DESCRIPTION					DATE		SHEET 1 of 1	
DATE					BY		DRAW	
CHK					APPD			
REVISIONS								